

EAST - [10.601868.wsp:1]

File View Edit Tools Window Help

Active

- L1: (41291) sense adj2 amplifier
- L2: (13611) reference adj2 cell
- L3: (166209) equaliz\$7
- L4: (3037992) compar\$7
- L5: (64358) wordline or word-line or (word adj line)
- L6: (1433) 1 same 2
- L7: (109) 3 same 6
- L8: (28) 4 same 7
- L22: (9) 5 same 8
- L23: (5038) 365/203
- L24: (3401) 365/205
- L25: (3087) 365/207
- L26: (2019) 365/208
- L27: (2191) 365/210
- L28: (512) 1 and 2 and 3
- L29: (430) 4 and 28
- L30: (443) 5 and 28
- L31: (375) 5 and 29
- L32: (443) 30 or 31
- L33: (96) 32 and 23
- L37: (87) 32 and 24
- L38: (112) 32 and 25
- L39: (64) 32 and 26
- L40: (126) 32 and 27
- L41: (422) 32 and "365"/\$6
- L42: (189) 41 not (33 or 37 or 38 or 39 or 40)
- L43: (109) (JEONG near2 JONG).in.

Failed

Saved

Search List Browse Queue Clear

DBs US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB

Plurals

Default operator: OR

Highlight all hit terms initially

BRS form IS&R form Image Text HTML

	U	1	3	2	Document ID	Issue Date	Inventor	Current OR	Current XRef	Retrieval CI	Title	Pages	S	C	P
1	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5138579 A	19920811	Tatsumi; Yuuichi et al.	365/185.25	365/185.13;		Semiconductor memory device	19	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5258959 A	19931102	Dallabora; Marco et al.	365/185.21	365/203		Memory cell reading circuit	8	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5559737 A	19960924	Tanaka; Sumio et al.	365/185.25	365/185.2;		Nonvolatile semiconductor mem	16	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5886925 A	19990323	Campardo; Giovanni et al	365/185.21	365/185.2		Read circuit and method for non	16	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6628548 B1	20030930	Hsu; Yu-Ming et al.	365/185.21	365/201;		Flash memory with accelerated	16	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

Hits Details HTML

Print the active document

Start C Pro... Re... E... R... L... E... U... 1... 1... 1... 1... 1... 1... 1... 1... E...

5:03 PM